TITLE: INTERRUPTER AND HYBRID-SWITCH TESTING FOR FUSION DEVICES

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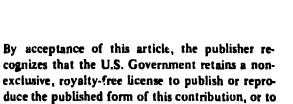
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INTERRUPTER AND HYBRID-SWITCH TESTING

by

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Summary

This paper discusses recent and ongoing switch testing for fusion devices. The first part describes testing for the TPTR ohmic-heating circuit. In this set of tests, which simulated the stresses produced during a plasma initiation pulse, circuit breakers were required to interrupt a current of 24 kA with an associated recovery voltage of 25 kV. Two interrupter systems were tested for over 1000 operations each, and both appear to satisfy TPTR requirements.

second part discusses hybrid- witch development for superconducting coil protection-These switching systems must be capable of carrying large currents on a continuous basis as well as performing interruption duties. One such switching system, rated at 25 kA and 2.5 kV, is currently being developed for the Large Coil Project at the Oak Ridge National Laboratory. This switch assembly consists of a high-current by-pass switch in parallel with a magnetic blowout interrupter. Another switch for this type of application is being developed for use with large tokamak induction coils. This switch is a vacuum interrupter with water-cooled electrodes. Test results at 13 kA continuous current and future plans for extending the steady-state rating to 25 kA are presented.

The third part presents preliminary results on an early-counterpulse technique applied to vacuum interrupters. Implementation of this technique has resulted in large increases in interruptible current as well as a marked reduction in contact erosion.

Introduction

The Los Alamos Scientific Laboratory (LASL) has been engaged in switch testing for fusion devices since 1974. At that time, one facility existed with an installed capability of 10 kA steady-state and 25 kA pulsed. Presently three "facilities are operative, the largest has an installed capacity of 100 kA steady-state and 280 kA pulsed. This increase has been necessary due to the larger and mise continuous current requirements conceived for mod in fusion devices such as FTF and INTOR.

TETP Obmic-Reating Circuit Switchgoar Tests

The Tokamak Fusion Test Reactor (TFTR) project at the Princeton Plasma Physics Laboratory requires the insertion of a resistor in an excited obsic-heating coll circuit to produce a plasma initiation pulse (PIP). The maximum duty for the switching system will

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Laboratory. Contributed to TETP testing only.

he an interruption of 24 kA with an associated recovery voltage of 25 kV. Vacuum interrupters were selected as the most econ mical means to satisfy these requirements. However, some testing of available systems needed to be performed to determine their reliability. 3

Westinghouse System

One system tested consisted of two series-connected Westinghouse WL-33552 interrupters with external axial-fields and a 75 μF counterpulse bank. These interrupters are shown in Fig. 1. The actuators us: were Ross Engineering Model HA-75. Figure 2 is a schematic of the circuit used to test these interrupters. The homopolar generator, HP, was used to supply $6\times 10^7\,\mathrm{A}^2-\mathrm{s}$ of heating prior to current interruption on each test. This closely simulated the heating seen by the interrupters during charging of the ohmic-heating coil. Figure 3 shows a partial current waveform for a typical test. The homopolar current (10 kA) is brought to zero a few milliseconds before the high-current pulse is initiated. The current level just prior to the counterpulse is 24 kA.

Over 1000 tests were performed on the Westinghouse interrupter system at 24 kA and 25 kV. Only one failure to interrupt was observed, this being partially actributed to a broken bolt in one actuator that was discovered just after that particular test. Three single-interrupter restrikes were observed during this testing, none of which led to an interruption failure. The total contact erosion during the more than 1000 interruptions was less than 0.010 in. indictating that a projected life of 104 operations is feasible.

Toshiba System

The other system tested was a Toshiba Model VGB2-D20 interrupter system. This system had two interrupers connected in series with axial-field coils built into the electrodes themselves. Both interrupters were actuated from a single spring-loaded mechanism supplied by Toshiba. A picture of this system in the test facility is shown in Fig. 4. The manufacturer specified the characteristics of the maturable reactor to be used during these tests. As a result of the large saturated inductance of this component, a 450 pF counterpulse bank was required for commutation.

A schematic of the circuit used for the Toshiba testing is shown in Fig. 5. The heating requirements prior to interruption were relaxed for this set of tests, so there was no homopolar generator in the test

Over 1000 tests were run on the Toohiba system at 24 kA and 25 kV. A typical current waveform showing an expansion of the reduced di/dt just prior to current zero is shown in Fig. 6. There were neither failures to interrupt nor single interruptor restrikes

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during any of these tests. The actuator stroke was carefully monitored for over 2000 operations and found to be extremely consistent. Total contact erosion during the tests was found to be less than 0-010 in-

In addition to these tests, permission was granted by Toshiba to test a single interrupter for its maximum interruptible current. This was found to be about 41 kA at 30 kV for a 90% reliability.

Superconducting Coil Protection Switches

Due to the large energies and the substantial capital investment associated with large superconducting inductors, protection devices are usually designed into the coil circuit that allow rapid dissipation of coil current in an emergency situation. A typical protection circuit consists of an interrupting switch and a dump resistor. When the interrupter operates, the coil current is diverted into the dump resistor while the charging power supply is disconnected. The coil current then decays with an associated L/R time constant.

Such a switching circuit will be tested for the Oak Ridge National Laboratory (ORNL) Large Coil Project (LCP). Figure 7 shows the details of the LCP circuit. Both the primary and back-up interrupters consist of a modified, commercially available, high-current, by-mass switch in parallel with a magnetic blowout de circuit breaker. The high-current by-mass switch handles the continuous 25 kA due to steady-state current limitations of the de interrupter. If the coil under test goes normal, or a failure occurs in the mower supply, belium liquifier, dewar, or other critical component, the by-mass switch opens and transfers the current to the de interrupter. The interrupter then operates and diverts the coil current into the dump resistor where it is quickly dissipated. LASL will be testing a prototype interrupter system for ORNL during mid 1980. Questions such as reliability, lifetime, and maintenance will be addressed during this testing program.

For the same general application, a special interrupter 1s currently being developed that requires no external by-mass witch. This is a vacuum interrupter with water-cooled electrodes and very high centact closing forces. Presently, the continuous current rating for a seven-inci interrupter has been increased from 1-2 kA to 13 kA. Implementation of a new high-pressure actuator developed at LASE and a modification in coatact material will hopefully increase the continuous current rating to 25 kA. The steady-state current rating will then match the interruption rating. The units may then be connected in pagallol for higher currents.

Farly Counterpulse Technique

The conventional approach towards current commutation in de Interrupters requires that the electrodes be separated substantially before the counterpulse is applied. This necessitates a period of full-current arcing during each interruption sequence. This arcing heats the electrodes and can cause hot spots to form on the electrode surfaces. Thermionic emission from these hot spots is helieved to cause reignition of the arc and limits the maximum istorruptible current for a particular device. In addition, this arcing period erodes the electrodes, causes pitting and deformation, and deposits it metallic film on shields and insulators within the vacuum space. Another method for commutation, called

the early counterpulse technique (FCT), shows promise not only of increasing the maximum current ratings for an interrupter but also of substantially decreasing electrode erosion. With this technique, the current is commutated to a near-zero level before the electrodes are separated. Figure 8 shows a comparison of the normal and early counterpulse current waveforms.

A four-inch vacuum interrupter whose normal maximum interruption level was about 6 kA was tested using ECT. This interrupter was able to interrupt currents as high as 25 kA. In addition, 1200 interruptions were performed at 10 kA with no failures. The interrupter was then opened and the contacts were inspected. Virtually no erosion had occurred. An extrapolated contact lifetime of 10 to 10 operations could be anticipated using this technique.

Implementation of FCT requires three special components. The first is a very fast actuator. The commutation bank must hold the interrupter current near zero while the contacts separate. A fast separation requires less time at low current levels and thus reduces the size of the commutation bank. The second component is a large saturable reactor to insure that the counterpulse current does not exceed the interruntion current by more than a few hundred amperes. The third component is a larger-than-normal counterpulse bank whose size, as mentioned, is determined by actuator speed.

Conclusions

The switching program has contributed to the national fusion effort in several ways. First, by testing commercial switching systems, such as those tested for use in TFTR. Secondly, by modifying commercial switches for special applications. The hybrid interrupter system to be tested for use in LCP at ORNL is an example of such a modification. Finally, research and development efforts are under way for future fusion devices by developing water-cooled interrupters for superconducting cell protection and the early-counterpulse technique for extending the lifetime of conventional interrupters.

Acknowledgment

The LASL staff is grateful for permission to perform extended tests on the Toshiba Interrupter.

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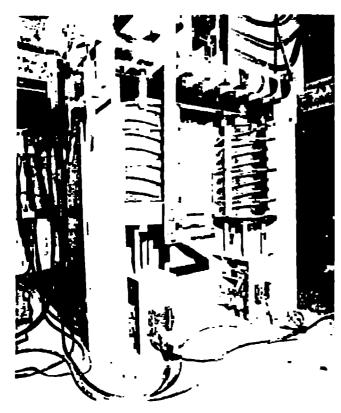
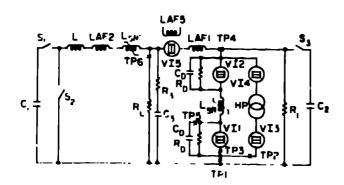


Fig. 1. Westforhouse interrestors.



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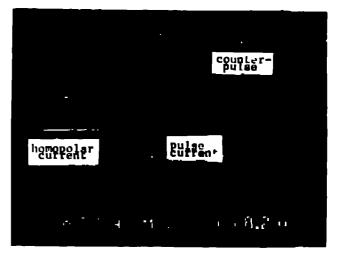
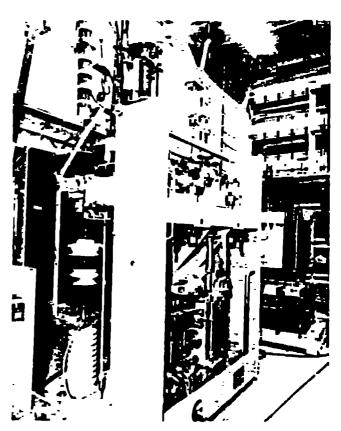
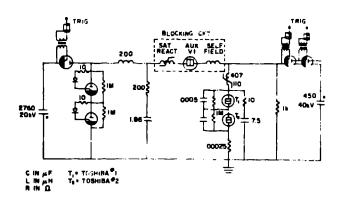


Fig. 3. Wostinchouse interrupter current waveform.



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F1q. 5. Toshiba interrupter test circuit.



Fig. 6.
Toshiba interrupter current waveform.
(a) Full waveform.
(b) Expansion near current zero.

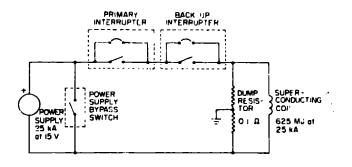


Fig. 7.
ORVL LOP coil protection circuit.

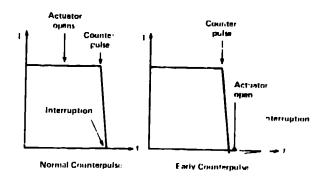


Fig. 8. Comparison of normal and early counterpulse current waveforms.